



PRODUCTION SPECIFICATION OF LCD MODULE

MODULE NO.: PV03403G0140D-CO

Customer Name:			
Customer Part Number:			
Approved By:		Date:	

Prepared By	Checked By	Approved By



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Revision History

Rev	Issued Date	Description	Page	Editor
1.0	Mar. 16, 2021	First release	All	
2.0	May.08.2021	Change Mechanical Drawing	P15	



1 General Specifications

	Feature	Specifications
Display Spec.	LCD type	3.4 inch
	Resolution (H*V)	480*(RGB)*480
	Technology Type	a-Si TFT
	Pixel Configuration	R.G.B. Vertical Stripe
	Display Mode	Normally Black
	Viewing Direction	ALL
	Gray Scale Inversion Direction	ALL
Mechanical Characteristics	OutlineDimensions (W x H x T) (mm)	76.50*76.50*3.02
	Active Area(mm)	60.48*60.48
	With /Without Touch screen	With CTP
	Match Connector Type	0.5 Pitch 40 Pin
	Backlight Type	White LED
	Weight (g)	TBD
Electrical Characteristics	Interface	SPI 3LINE+18BITRGB
	Number of color	16.7M
	Driver IC	ST7701S
	Touch Driver IC	FT6336U



2 Pin Assignment

NO.	PIN NAME	Description
1	LEDA	LED anode
2	LEDK1	LED Cathode
3	LEDK2	LED Cathode
4	GND	Ground
5	VCI	Power Supply 2.8V
6	RESET	LCM reset signals
7	NC	Dummy
8	NC	Dummy
9	SDA	SPI Interface Input Pin
10	SCL	This pin is used to be serial interface clock.
11	CS	Input pin for chip selection signal
12	PCLK	Clock Signal to Sample each date
13	DE	Data enable signal for RGB interface operation
14	VS	Vertical Synchronizing Signal
15	HS	Horizontal Synchronizing Signal
16~33	DB0~DB17	Display Data I/O
34	GND	Ground
35	TP_INT	Touch Interrupt
36	TP_SDA	Touch IIC Data signal
37	TP_SCL	Touch IIC Clock signal
38	TP_RESET	Touch Reset Signal
39	TP_VCI	Touch Power supply
40	GND	Ground

Note1: I/O definition: I-----Input O---Output P----Power/Ground



3 Absolute Maximum Ratings

GND=0V, Ta= 25°C

Item	Symbol	Value	Unit
Power supply voltage for logic	V _{DD}	0.3~3.6	V
Input voltage	V _{in}	V _{DD} +0.3	V
Operating temperature	T _{opr}	-10 to 60	°C
Storage temperature	T _{stg}	-20 to 70	°C

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

GND=0V, Ta=25°C

Item	Symbol	Min	Type	Max	Unit	Test condition
Operating voltage	V _{DD}	2.6	2.8	3.3	V	-
Supply current	I _{DD}	-	-	20	mA	V _{DD} =2.8V, Ta=25°C
Input voltage	V _{IH}	0.8V _{DD}	-	V _{DD}	V	-
	V _{IL}	0	-	0.2V _{DD}	V	
Input leakage current	I _{IL}	-1.0	-	1.0	μA	V _{IN} =V _{DD} or V _{SS}

Note: Voltage greater than above may damage the module.

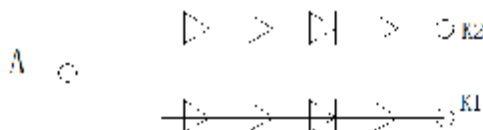
All voltages are specified relative to VSS=0V.

4.2 Driving Backlight

Ta=25°C

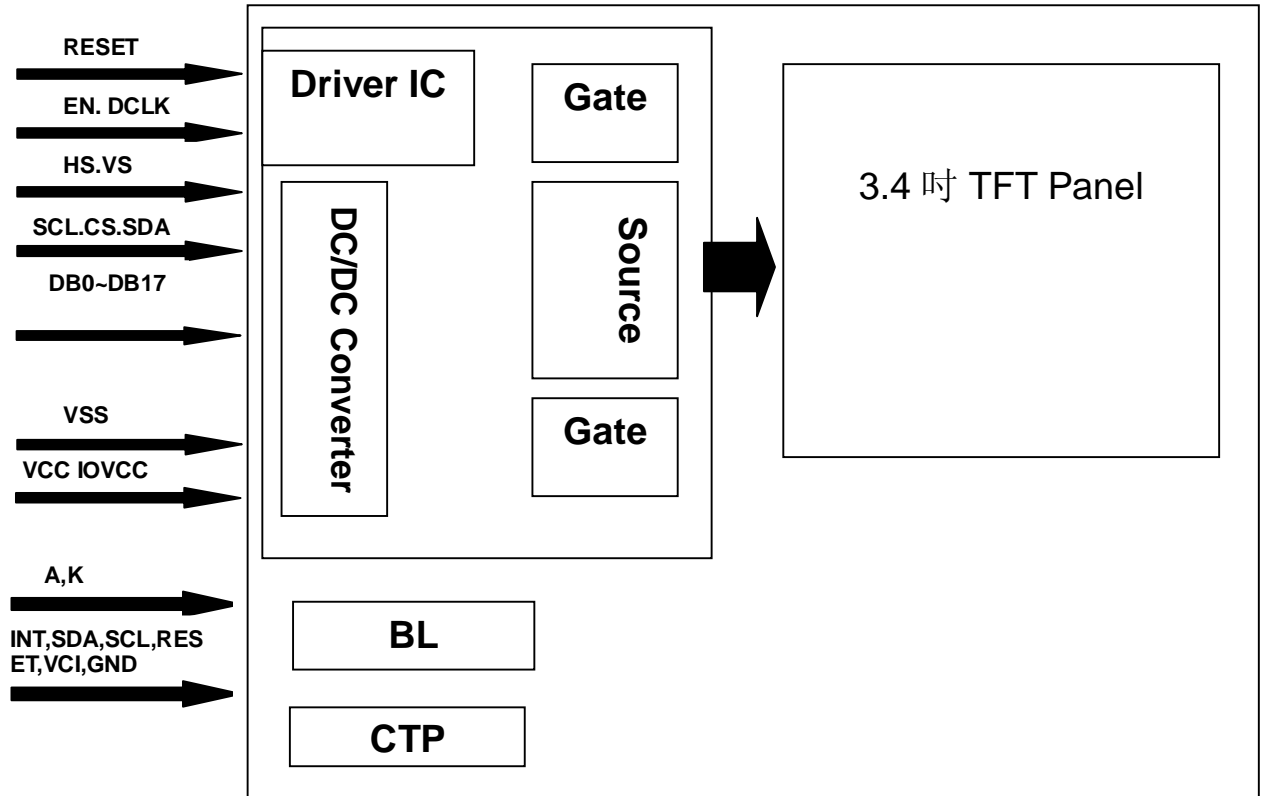
Item	Symbol	Min	Typ	Max	Unit	Remark
Forward Current	I _F	--	40	45	mA	
Forward Voltage	V _F	-	12.8	-	V	
Connection mode	P	--	4S2P	--		
LED number	/		8		pcs	
LED life time		20000			hours	

Note1: Optical performance should be evaluated at Ta=25°C only .If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.





4.3 Block Diagram





5 INTERFACE TIMING

5.1 System Bus Read/Write Characteristics.

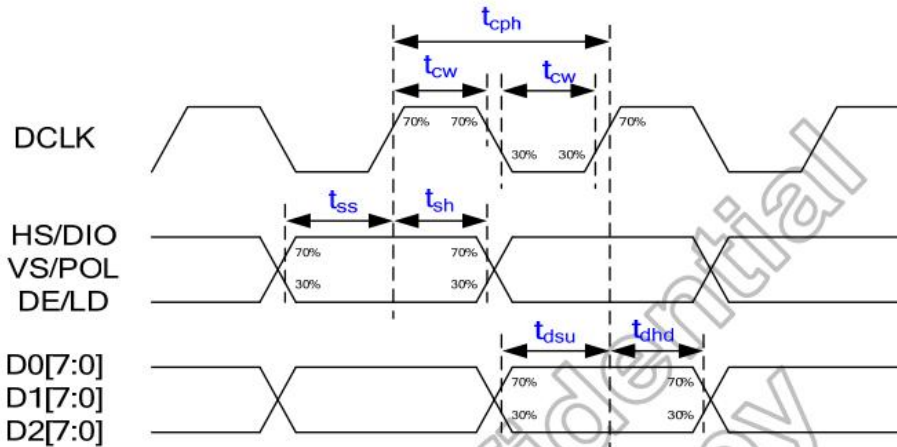


Figure 6.10: Input signal timing

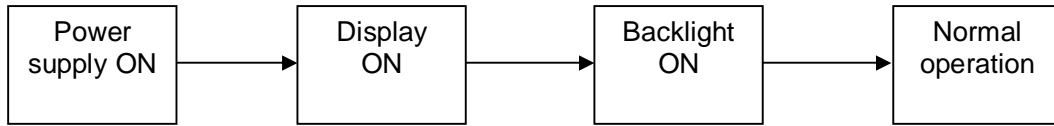
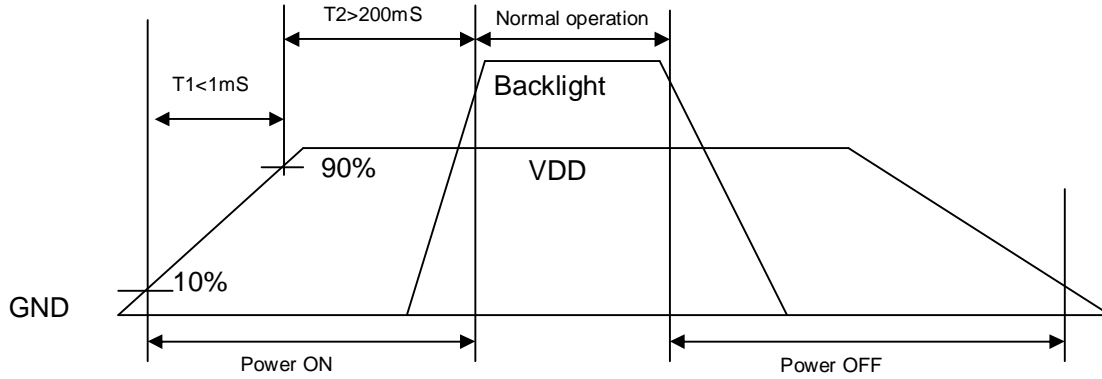
Input data/Sync. Parameters

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK period	T_{cph}	12	-	-	ns
DCLK duty ratio	T_{cw}	40	50	60	%
Data setup time	T_{dsu}	5	-	-	ns
Data hold time	T_{dhd}	5	-	-	ns
VS/POL setup time	T_{ss}	5	-	-	ns
VS/POL hold time	T_{sh}	5	-	-	ns
HS/DIO setup time	T_{ss}	5	-	-	ns
HS/DIO hold time	T_{sh}	5	-	-	ns
DE/LD setup time	T_{ss}	5	-	-	ns
DE/LD hold time	T_{sh}	5	-	-	ns

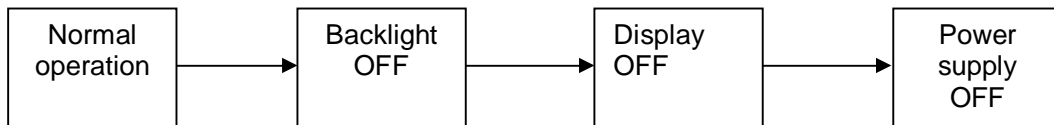
Note: (1) Input signals can only be triggered by rising edge of DCLK in dumb source mode.



5.2 Power ON/OFF Timing



Power ON sequence



Power OFF sequence



6 Optical Characteristics

Ta=25°C

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles	θT	CR ≥ 10	75	85	-	Degree	Note 2
	θB		75	85	-		
	θL		75	85	-		
	θR		75	85	-		
Contrast Ratio	CR	θ=0°	800	1000	-	-	Note1 Note3
Response Time	T _{ON}	25°C	-	30	35	ms	Note1
	T _{OFF}						Note4
Uniformity	U	-	70	80	-	%	Note1 Note6
NTSC	-	-	-	50	-	%	Note 5
Luminance	L		-	250	-	cd/m ²	Note1 Note7

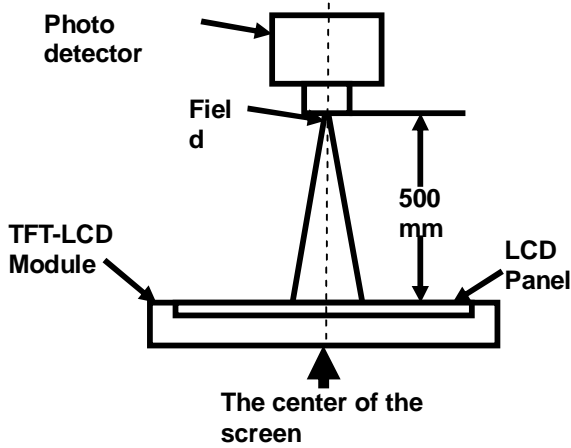
Test Conditions:

- VF=12.8V, IF=40mA, the ambient temperature is 25°C.
- The test systems refer to Note 1 and Note 2.



Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

Note 2: Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80).

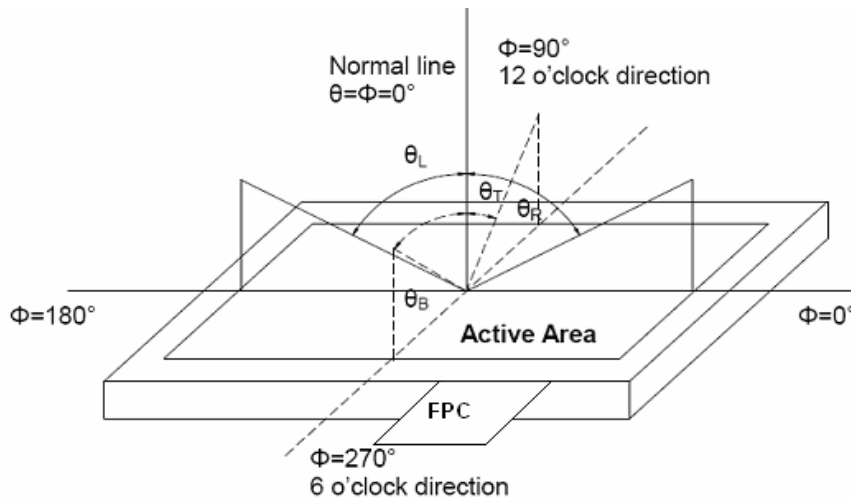


Fig. 1 Definition of viewing angle



Note 3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

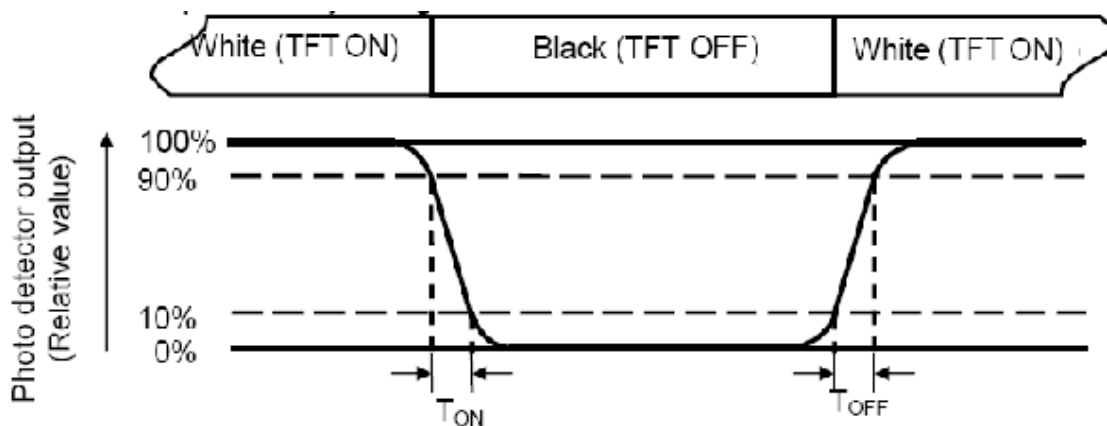
“White state “:The state is that the LCD should be driven by Vwhite.

“Black state”: The state is that the LCD should be driven by Vblack.

Vwhite: To be determined Vblack: To be determined.

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.



Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.



Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (U)} = L_{\min} / L_{\max}$$

L-----Active area length W----- Active area width

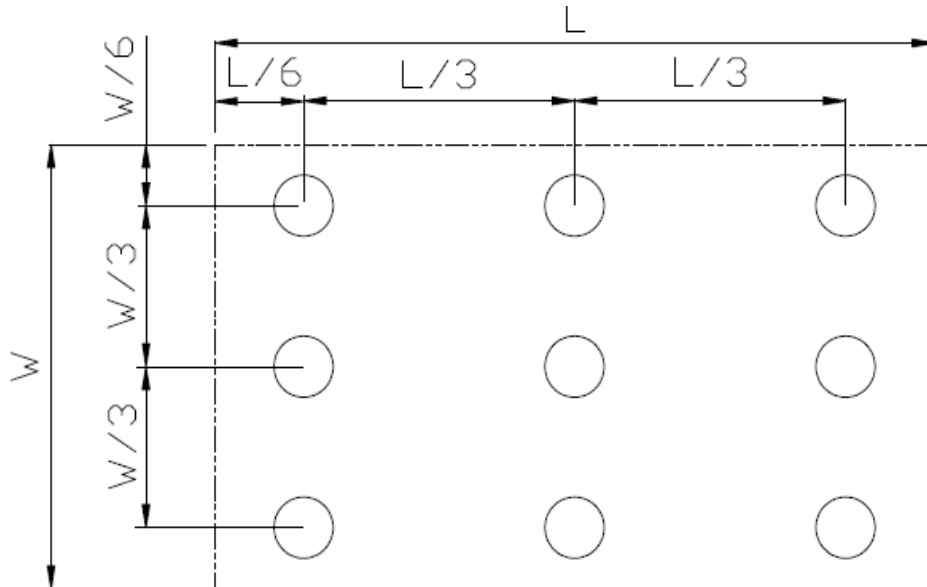


Fig. 2 Definition of uniformity

L_{\max} : The measured maximum luminance of all measurement position.

L_{\min} : The measured minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.



7 Environmental / Reliability Test

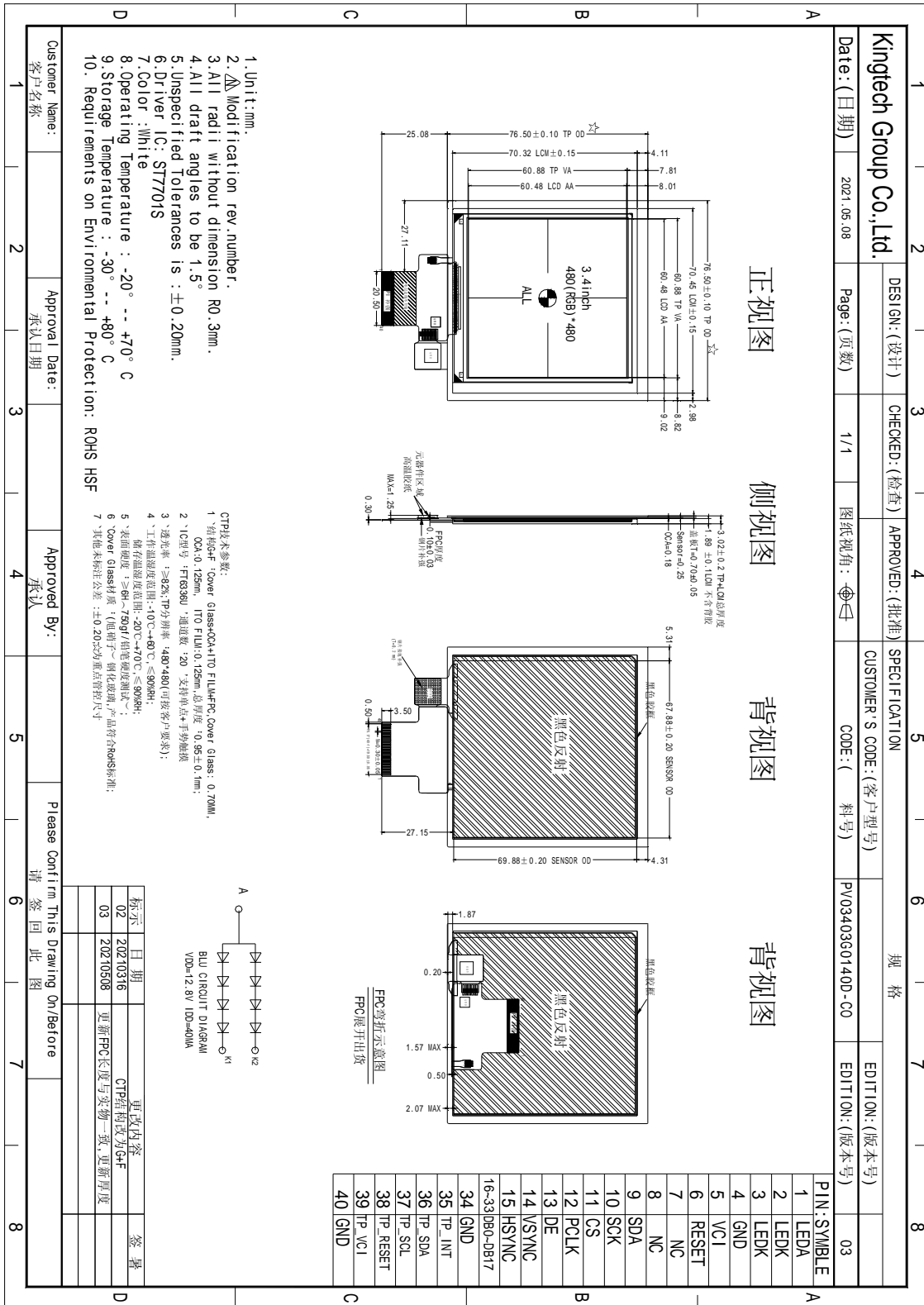
Item	Condition	Time (hrs)	Assessment
High temp. Storage	70°C	120	No abnormalities in functions and appearance
High temp. Operating	60°C	120	
Low temp. Storage	-20°C	120	
Low temp. Operating	-10°C	120	
Humidity	40°C/ 90%RH	120	
Thermal Shock(Non-operation)	-20°C ← 25°C →70°C (0.5 hour ← 5 min → 0.5 hour)	10cycles	

Remark:

- 1.The test samples should be applied to only one test item.
- 2.Sample size for each test item is 1~10pcs.
- 3.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.



8 Mechanical Drawing





9 Precautions For Use of LCD Modules

9.1 Handling Precautions

9.1.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

9.1.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

9.1.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

9.1.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

9.1.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

9.1.1.6 Do not attempt to disassemble the LCD Module.

9.1.1.7 If the logic circuit power is off, do not apply the input signals.

9.1.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

9.1.1.9 Be sure to ground the body when handling the LCD Modules.

9.1.1.10 Tools required for assembly, such as soldering irons, must be properly ground.

9.1.1.11 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

9.1.1.12 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

9.1.1.13 Storage precautions

9.1.1.14 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

9.1.1.15 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

9.1.1.16 The LCD modules should be stored in the room without acid, alkali and harmful gas.

9.2 Transportation Precautions

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.