



承认书

产品型号: DW1563B5Z0

产品规格: 1920RGB * 1080 INNO TFT 液晶显示屏

送承方: _____

制订	审查	核准	公司印章
 2023.03.30		 2023.03.30	

承认方: _____

研发部	工程部	品质部	核准	公司印章

临时规格书

正式规格书



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1. FOB Specification

1.1 Description

DW1563B5Z0 is a transmissive type color active matrix liquid crystal display(LCD) which uses amorphous thin film transistor(TFT) as switching devices. This product is composed of a TFT LCD panel, 4 drives IC, a PCBA and a LED-backlight unit. The active display area is 15.6 inches diagonally measured and the native resolution is 1920*RGB*1080. Features of this product are listed in the following table.

1.2 Functions & Features

Table 1.1 Module Functions & Features

Parameter	Value	Unit
LCD Mode	TFT/Transmissive	-
Color Depth	16.7M	-
Display Resolution	1920RGB*1080	pixels
Module Size	211.6(H)*359.3(W)*6.33(T)(Exclude PCBA)	mm
Active Area (A.A)	193.59(H)*344.16(W)	mm
Pixel Arrangement	RGB-stripe	-
Viewing Direction	ALL	
Display Mode	Normally Black	
LCD Controller/Driver	ILI6145A	-
IC Package Type	COG	-
Interface	LVDS	-
Power Supply Voltage	5.0	V
LCM Brightness	600	cd /m ²
Back-light	60	PCS



2. Mechanical Specification

PIN NO.	SYMBOL
1	GND
2	RxD0-
3	RxD0+
4	RxD1-
5	RxD1+
6	RxD2-
7	RxD2+
8	GND
9	RxDCLK-
10	RxDCLK+
11	GND
12	RxD3-
13	RxD3+
14	RxE0-
15	RxE0+
16	RxE1-
17	RxE1+
18	RxE2-
19	RxE2+
20	GND
21	RxECLK-
22	RxECLK+
23	GND
24	RxE3-
25	RxE3+
26	GND
27	VDD 5.0V
28	VDD 5.0V
29	VDD 5.0V
30	GND

PCB厚度 1.0mm (LVDS接口)
FOB组裝后包胶

LCM Brightness	550cd/m ² (MIN) 600 cd/m ² (TYP)
Display Type	TFT/NormallyBlack/TRANSMISSIVE
Display Resolution	DOTS:1920RGB*1080
Viewing Angle	ALL
LCM Controller/Driver	ILI16145A / MST7332L
Logic Voltage	5.0V
Operation Temperature	0°C ~ 70°C
Storage Temperature	-30°C ~ 80°C
Backlight Spec.	White LED (10 dies in series 6 parallel I=360mA V=27~32V)

REV.	DESCRIPTION	DATE	APPROVE:
0.1	新制訂	2023.03.30	

Kingtech Group Co.,Ltd.	
TITLE:	DW1563B5Z0
DOC. NO.	
PART NO.	
REV.	0.1
SHEET	1 OF 1
SCALE	1:1
UNITS:	mm

0.1 新制訂 2023.03.30

REV. DESCRIPTION DATE APPROVE:

0.1 新制訂 2023.03.30

REV. DESCRIPTION DATE APPROVE:

0.1 新制訂 2023.03.30

REV. DESCRIPTION DATE APPROVE:





3. Pin Descriptions

Pin No.	Symbol	I/O	Functional	Remark
1	GND	P	System ground.	
2	RxO0-	I	LVDS RX PHY Port-A Data Lane-0 Negative Input	
3	RxO0+	I	LVDS RX PHY Port-A Data Lane-0 Positive Input	
4	RxO1-	I	LVDS RX PHY Port-A Data Lane-1 Negative Input	
5	RxO1+	I	LVDS RX PHY Port-A Data Lane-1 Positive Input	
6	RxO2-	I	LVDS RX PHY Port-A Data Lane-2 Negative Input	
7	RxO2+	I	LVDS RX PHY Port-A Data Lane-2 Positive Input	
8	GND	P	System ground.	
9	RxOCLK-	I	LVDS RX PHY Port-A Data Clock Lane Negative Input	
10	RxOCLK+	I	LVDS RX PHY Port-A Data Clock Lane Positive Input	
11	GND	P	System ground.	
12	RxO3-	I	LVDS RX PHY Port-A Data Lane-3 Negative Input	
13	RxO3+	I	LVDS RX PHY Port-A Data Lane-3 Positive Input	
14	RxE0-	I	LVDS RX PHY Port-B Data Lane-0 Negative Input	
15	RxE0+	I	LVDS RX PHY Port-B Data Lane-0 Positive Input	
16	RxE1-	I	LVDS RX PHY Port-B Data Lane-1 Negative Input	
17	RxE1+	I	LVDS RX PHY Port-B Data Lane-1 Positive Input	
18	RxE2-	I	LVDS RX PHY Port-B Data Lane-2 Negative Input	
19	RxE2+	I	LVDS RX PHY Port-B Data Lane-2 Positive Input	
20	GND	P	System ground.	
21	RxECLK-	I	LVDS RX PHY Port-B Data Clock Lane Negative Input	
22	RxECLK+	I	LVDS RX PHY Port-B Data Clock Lane Positive Input	
23	GND	P	System ground.	
24	RxE3-	I	LVDS RX PHY Port-B Data Lane-3 Negative Input	
25	RxE3+	I	LVDS RX PHY Port-B Data Lane-3 Positive Input	
26	GND	P	System ground.	
27-29	VCC_5.0V	P	Power Supply, 5.0V (typ.)	
30	GND	P	System ground.	



4. Electrical Units

4.1 Absolute Maximum Ratings

The absolute maximum ratings are list on Table 4.1. When used out of the absolute maximum ratings, the LCM may be permanently damaged. Using the LCM within the following electrical characteristics limit is strongly recommended for normal operation. If these electrical characteristic conditions are exceeded during normal operation, the LCM will malfunction and cause poor reliability.

Table 4.1 Module Absolute Maximum Ratings

Item	Symbol	Unit	Value	Note
Power Supply Voltage (1)	Vdd	V	-0.3 to +6.0	
Power Supply Voltage (2)	VGH ~ VSS	V	-0.3 to +25	
Power Supply Voltage (3)	VSS ~ VGL	V	0 to -10.0	
Operating Temperature	Top	°C	0 to +70	
Storage Temperature	Tst	°C	-10 to +70	
Operating Humidity	Hop	%(RH)	10~85	

(VSS=0V)

4.2 Electrical characteristics (Ta=25°C)

Table 4.2:DC Characteristic (Vcc = 4.5 ~ 5.5V)

Item		Symbol	Condition	Min.	Type.	Max.	Unit
Supply Voltage	Logic	Vdd	---	4.5	5.0	5.5	V
Input Voltage	H level	V _{IH}	---	--	---	50	mV
	L level	V _{IL}		-50	---	--	
Current Consumption		I _{DD}	With internal voltage generation; VDD=5.0V; Tamb=25°C;	---	TBD	---	mA



4.3 Back-light Specification

Table 4.3 Back-light Characteristics

Item	Symbol	Conditions	Min.	Type.	Max.	Unit
Supply Voltage	VF	Only Backlight	26	30	34	V
Supply Current	IF		360			mA
Average Brightness	IV	Backlight Current IF=360mA	6600	7200	8500	Cd/ m ²
CIE Color Coordinate (Without LCD)	X	Backlight Current IF=360mA	0.303	--	0.313	-
	Y		0.324	--	0.336	
Uniformity	B	Backlight Current IF=360mA	--	80%	-	%
Color	White					

Note: 10 LEDs in series 6 parallel connection.

5. AC Characteristics

LVDS Receiver AC Specifications					
Symbol	Parameter	Min	Typ	Max	Unit
CLK	Output clk cycle	6.25	Tc	37.0	ns
t _{rise}	VOD rise time, 20% to 80%	250	350	500	ps
t _{fall}	VOD fall time, 20% to 80%	250	350	500	ps
T ₀	Input data position0	-0.15	0	0.15	ns
T ₁	Input data position1	Tc/7-0.15		Tc/7+0.15	ns
T ₂	Input data position2	2Tc/7-0.15		2Tc/7+0.15	ns
T ₃	Input data position3	3Tc/7-0.15		3Tc/7+0.15	ns
T ₄	Input data position4	4Tc/7-0.15		4Tc/7+0.15	ns
T ₅	Input data position5	5Tc/7-0.15		5Tc/7+0.15	ns
T ₆	Input data position6	6Tc/7-0.15		6Tc/7+0.15	ns
R _{term}	Termination Resister	80	100	125	Ω



6. Power Consumption

Resolution	Input	Output	Power Consumption (mA)
1366x768	2-Port 8-bit Mode LVDS	1-Lane/2.7G/Level0	245
		1-Lane/2.7G/Level1	259
		1-Lane/2.7G/Level2	255
		1-Lane/2.7G/Level3	260
1080P	2-Port 8-bit Mode LVDS	2-Lane/2.7G/Level0	281
		2-Lane/2.7G/Level1	291
		2-Lane/2.7G/Level2	298
		2-Lane/2.7G/Level3	309



7. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the TFT-LCD surface at a viewing angle of Φ and θ equal to 0° .

Measurement condition: Refer to next pages (C-light source, Halogen Lamp)

*1): with Polarizer *2): without Polarizer *3): Only Color Filter glass

Item	Symbol	Conditions	Specifications			Unit	
			Min.	Typ.	Max.		
Transmittance (w/o APCF, w/o Haze) (w/o WPA)	T%	Viewing normal angle $\theta_x = \theta_y = 0^\circ$	--	6.38	--	%	
Contrast Ratio (w/o WPA)	CR		800	1000	--	--	
Response Time (w/o WPA)	$T_{on} + T_{off}$		--	--	30	ms	
Viewing Angle	Hor.	θ_{x+}	--	80	--	deg.	
		θ_{x-}	--	80	--		
	Ver.	θ_{y+}	--	80	--		
		θ_{y-}	--	80	--		
CF only Color Chromaticity (CIE 1931)	Red	Rx	0.577	0.597	0.617	-	
		Ry	0.314	0.334	0.354	-	
	Green	Gx	0.274	0.294	0.314	-	
		Gy	0.498	0.518	0.538	-	
	Blue	Bx	0.122	0.142	0.162	-	
		By	0.131	0.151	0.171	-	
	White	Wx	0.286	0.306	0.326	-	
		Wy	0.308	0.328	0.348	-	
	NTSC% (x, y)			34	44		%



Notes : 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the center point across the TFT-LCD surface 500mm from the surface with all pixels displaying white. For more information see FIG 1.
3. Response time is the time required for the display to transition from white to black(Rise Time, Tr) and from black to white(Falling Time, Tf). For additional information see FIG 3.
4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the TFT-LCD surface. For more information see FIG 4.
5. Optimum contrast is obtained by adjusting the TFT-LCD Threshold voltage(Vth & Vsat)

FIG. 1 Optical Characteristic Measurement Equipment and Method

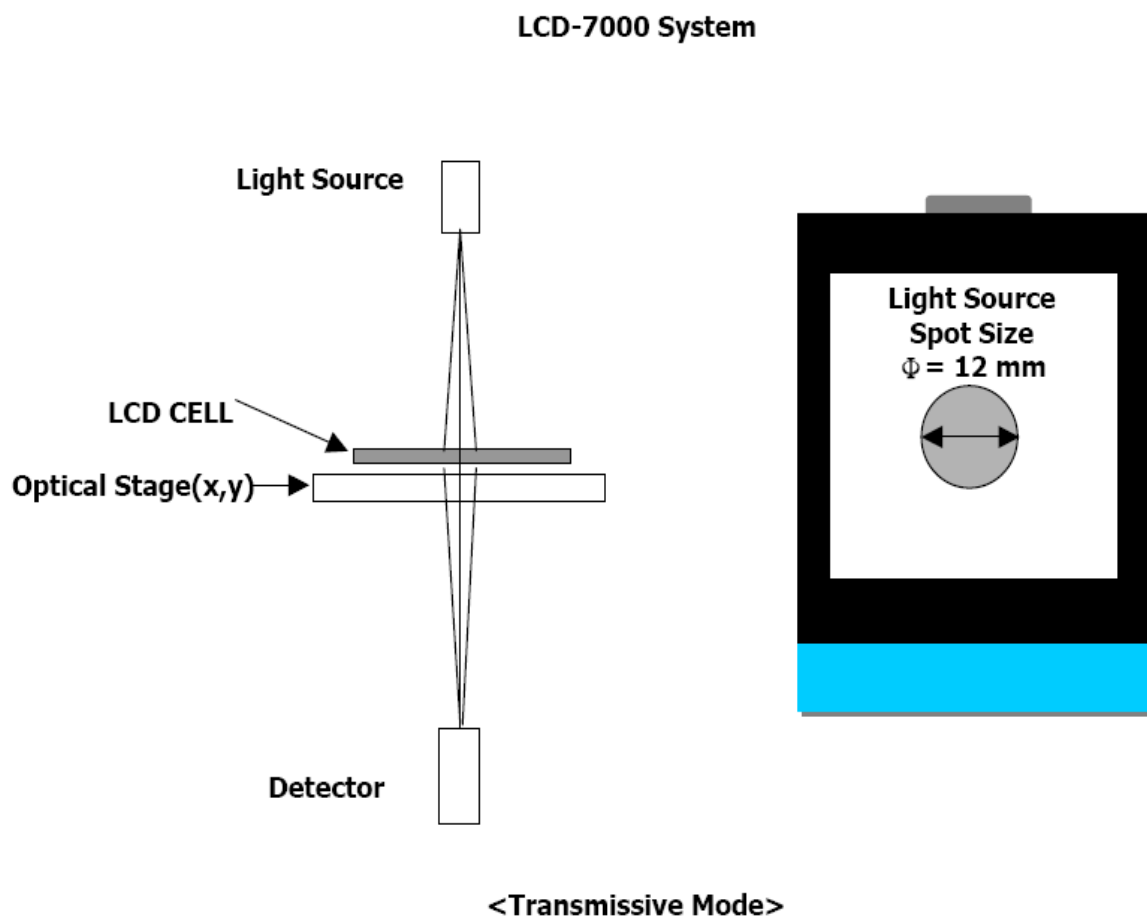




FIG. 2 The definition of V_{th} and V_{sat}

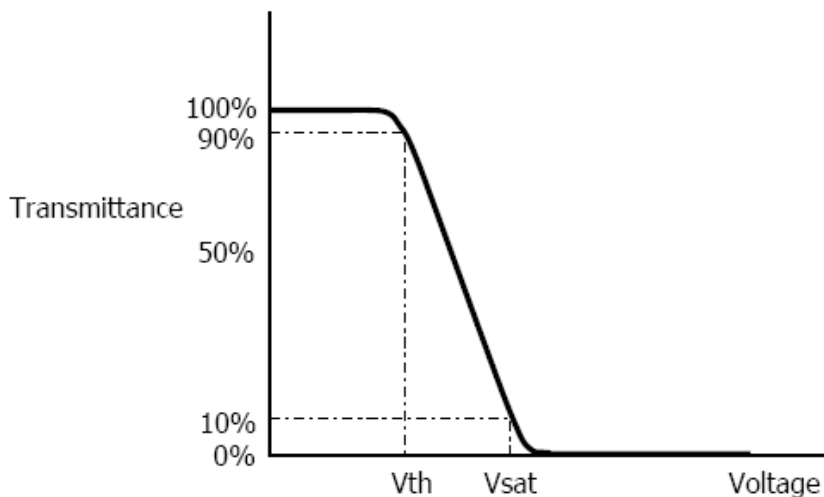
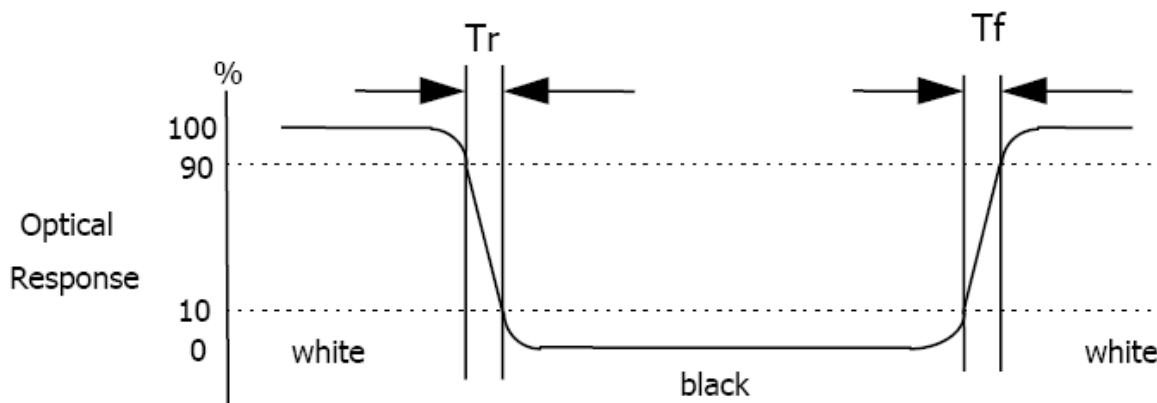


FIG. 3 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



* Voltage conditions for Response time

V_{gate} : 19V DC

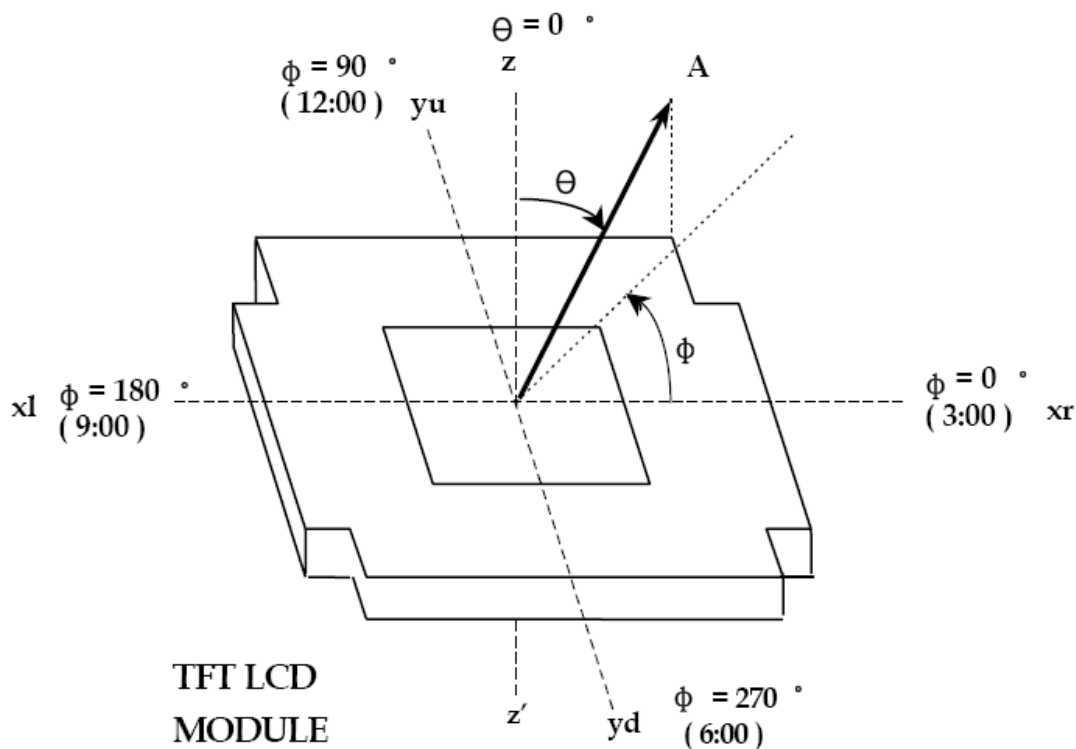
V_{data} : 0V~3.3V DC

V_{com} : 0V (Ground)



FIG. 4 The definition of viewing angle

<dimension of viewing angle range>



8. Reliability Test Items

No.	Test Item	Test Condition	Check Time
1	High temp storage	T=80°C	96Hrs
2	Low temp storage	T=-30°C	96Hrs
3	High temp operation	T=70°C	96Hrs
4	Low temp operation	T=0°C	96Hrs
5	High temp & high humidity	T=50°C H=90%	96Hrs

Reliability Test Criteria:

Display function should be no change under normal operating condition.



9. Handling Precautions

9.1 Safety

The liquid crystal in the LCD is poisonous. Keep away from your mouth and eyes. If the liquid crystal contacts with your skin, mouse or clothes, use soap to wash it off immediately.

9.2 Handling

- i. The LCD panel is made of very thin glass. Mechanical impact or extrusion to the surface should be prevented.
- ii. The polarizer attached on the display is very easy to be damaged, handle it with special attention.
- iii. To avoid contamination on the display surface, do not touch the display surface with bare hands.
- iv. The transparent electrodes may be disconnected if you use the LCD panel under dew-condensing environment.
- v. The characteristics of the semiconductor devices may be affected when they are exposed to light, possibly resulting in malfunctioning of the ICs. To prevent such malfunctioning of the ICs, make sure the application and the mounting of the panel are designed so that the IC is not exposed to light.

9.3 Static Electricity

Ground soldering iron tips, tools and testers when you operate. Also ground your body when handling the products and store the products in an anti-electrostatic container.

9.4 Storage

Store the products in a dark place where the temperature is within the range of $25\pm 10^{\circ}\text{C}$ and with low humidity (60%RH or less). Do not store the LCD product in an atmosphere containing organic solvents or corrosive gases.

9.5 Cleaning

Do not wipe the polarizer with dry cloth, as it might cause scratching. Wipe the polarizer with a soft cloth soaked with petroleum IPA. Other chemical might damage the panel.



10. QC

10.1 目的

制定 15.6 寸模组产品出货检验标准，明确检验内容和规范；

10.2 范围

使用原装模组、FOG或自购LCD所生产的模组、FOG出货；

10.3 定义

黑白点：显示的点是黑色或者白色，当对比变化时，这些缺陷在大小或强度上没有变化；

亮点：显示中的点（子像素），在显示区域显示为明亮，大小大于 1/2dot，通过 5%的 ND 卡在黑色画面下可见；

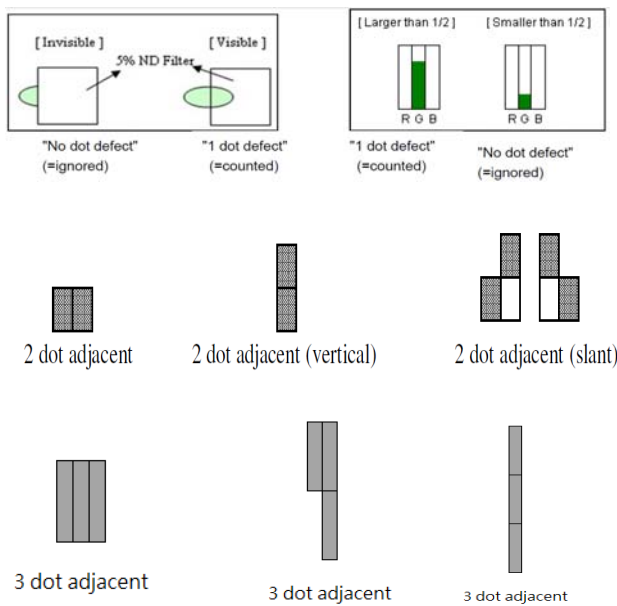
碎亮点：在黑画面下看到的发亮的，大小小于 1/2dot 的点；

暗点：显示中的点（子像素），大小大于 1/2dot，在红绿蓝画面的显示区域显示为黑色；

Mura：相对于显示区域的部分背景亮度，部分区域更暗或者更亮的现象（显示画面不均匀）；

二连点：两个相邻的 dot 点；

三连点：三个相邻的 dot 点；



10.4 检验条件

10.4.1 环境要求：温度：25±5℃、湿度：30%–70%RH；

10.4.2 外观检验灯光：20w 荧光灯或等效照度；

10.4.3 检验距离：30cm；

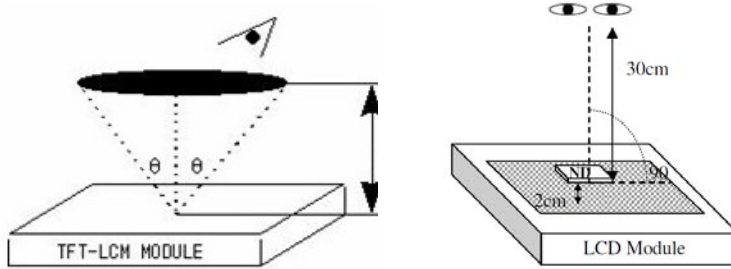


10.4.4 电性检验照度要求: 100-200Lux;

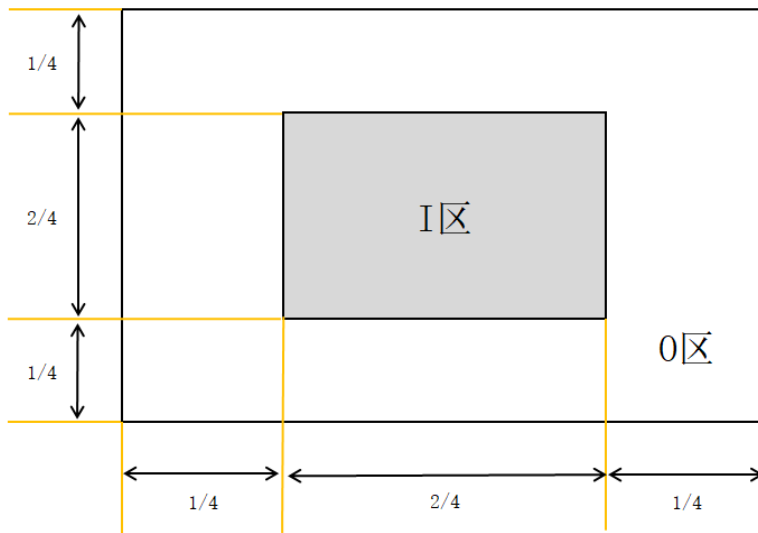
10.4.5 外观检验照度要求: 500-700Lux;

10.4.6 ND 卡检验要求: ND 放于距 cell 表面 2cm 处, 从 30cm 距离检验;

10.4.7 检验角度: 做垂直方向 $\pm 45^\circ$, 水平方向 $\pm 45^\circ$ 夹角检验;



10.4.8 显示区域划分: 将显示区长度和宽度方向均匀分为 4 等分, 中间 2/4 区域为 I 区, 四周为 0 区, 见下图:





10.5 电性检测

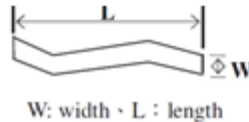
检验项目	检验标准																							
	A0 规			A1 规			A2 规			A3 规			A4 规			B 规								
	I 区	O 区	总数	I 区	O 区	总数	I 区	O 区	总数	I 区	O 区	总数	I 区	O 区	总数	I 区	O 区	总数						
亮点	N≤0			N≤2			N≤4			N≤5			N≤8			N≤10			不计					
二线亮点	N≤0			N≤0			N≤1			DS V			N≤5			N≤10			不计					
三线亮点	N≤0			N≤0			N≤0			EM			N≤5			N≤10			不计					
暗点	N≤1	N≤5, DS W 10mm	N≤5, DS W 10mm	N≤5, DS W 10mm			N≤5, DS W	N≤7			N≤7, DS W	N≤10			N≤20			不计						
二线暗点	N≤0			N≤1			10mm			N≤2, DS W 5mm			EM			N≤7			N≤20			不计		
三线暗点	N≤0			N≤0			N≤1			N≤1			N≤7			N≤20			N≤20			不计		
亮点+暗点	N≤5, DS ≥10mm			N≤5, DS ≥10mm			N≤7, DS ≥5mm			/			/			/			/					
脏污点	10μm 脏污不可见, 无视			5μm 脏污不可见, 无视			不计 (脏污量不可)			不计 (脏污量不可)			不计 (脏污量不可)			不计 (脏污量不可)			不计					
点状异物 黑点	D≤0.15mm, 数量 N 不计; 0.15mm<D≤0.3mm, N≤3, 间隔≥10mm; 0.3mm<D≤0.4mm, N≤1			D≤0.3mm, 数量 N 不计; 0.3mm<D≤0.8mm, N≤3, 间隔≥10mm;			D≤0.5mm, 数量 N 不计; 0.5mm<D≤0.8mm, N≤3, 间隔≥10mm; 0.8mm<D≤1.2mm, N≤1;			/			/			/			/					
线状异物	W≤0.1mm, L≤0.3mm, 不计; W≤0.1mm, L≤2mm, N≤2, DS≥10mm;			W≤0.1mm, 不计; 0.1mm<W≤0.2mm, 0.8mm<L≤4mm, N≤3;			W≤0.1mm, 不计; 0.1mm<W≤0.3mm, 0.8mm<L≤5mm, N≤3;			W≤0.1mm, 不计; 0.1mm<W≤0.5mm, 0.8mm<L≤4mm, N≤5;			/			/			/					
POL 气泡	D≤0.2mm, 不计; 0.2mm<D≤0.8mm, N≤2, DS≥10mm 线状按线状异物判定			线状及点状不良参照异物标准; 边缘气泡不进入 EM 区 1/2 为 OK			线状及点状不良参照异物标准; 边缘气泡不进入显示区为 OK			/			/			/			/					
Mura	1. 全视角判定, 距离 30mm-50mm 不可见为 OK 2. 轻微 mura 按 10μm 脏污不可见为 OK			MOS 和灰阶范围不可见, 漏背景可见, 不计			5μm 脏污不可见			5μm 脏污不可见			5μm 脏污不可见			5μm 脏污不可见			5μm 脏污不可见					
功能缺陷	显示异常、断线(横线、竖线等)、无品、等影响功能的不良不可有																							



10.6 外观检验

检验项目	图示	检验标准					
		A0 级	A1 级	A2 级	A3 级	A4 级	B 级
FAD 区角落破损		$X \leq 1.5mm, Y \leq 1.5mm, Z \leq T, N \leq 2$ 不伤及线路, 显示正常			不伤及线路, 显示正常		
FAD 区边缘破损		$T \leq 0.3, X \leq 2.0, Z \leq T, N \leq 2$ 不伤及线路, 显示正常			不伤及线路, 显示正常		
非 FAD 区角落破损		$X \leq 1.5mm, Y \leq 1.5mm, Z \leq T, N \leq 2$ 不伤及线路, 显示正常			不伤及线路, 显示正常		
非 FAD 区边缘破损		$X \leq 1.5mm, Y \leq 0.3mm, Z \leq T, N$ 不计, 不伤及线路, 显示正常			不伤及线路, 显示正常		
表面点	无图	$D \leq 0.3mm$, 数量 N 不计; $0.3mm < D \leq 0.8mm, N \leq 3$, 间距 $\geq 10mm$;			不计		
表面划伤	无图	$W \leq 0.1mm$, 不计; $0.1mm < W \leq 0.2mm, 0.8mm < L \leq 4mm, N \leq 3$;			不计		
FPC/PCB	无图	<ol style="list-style-type: none"> 定位孔破损不可有 FPC 死折不可有 元件脱落不可有 元件虚焊、溢锡等焊接不良不可有 金手指断、裂不可有 元件卡不到位不可有 FPC 锡胶对位偏移不可有 FPC 上双面胶脱落或异常不可有 插座不能松动, 不影响插拔 			<ol style="list-style-type: none"> 元件脱落, 不可有 元件虚焊、溢锡等焊接不良, 不可有 		
保护膜	无图	<ol style="list-style-type: none"> 划伤不可有, 其他划伤、印记等无视 易擦除贴膜痕附 			有保护膜		
裂纹		不可有					

1. W : Width
2. L : Length
3. D : Average Diameter
4. N : Count



10.7 质保期限

10.7.1 保修 12 个月（如有合同，以合同为准）。